



JHD12864-COG06B SPECIFICATION

DOC.REVISION A01

Customer Approval:

| |
|--|
| |
|--|

| | NAME | SIGNATURE | DATE |
|-------------|------|-----------|---------------------------|
| PREPARED BY | | | 30 th Mar 2010 |
| APPROVED BY | | | |



DOCUMENT REVISION HISTORY

| Version | DATE | DESCRIPTION | CHANGED BY |
|---------|-------------|-------------|------------|
| A00 | 16-Mar-2010 | First issue | |

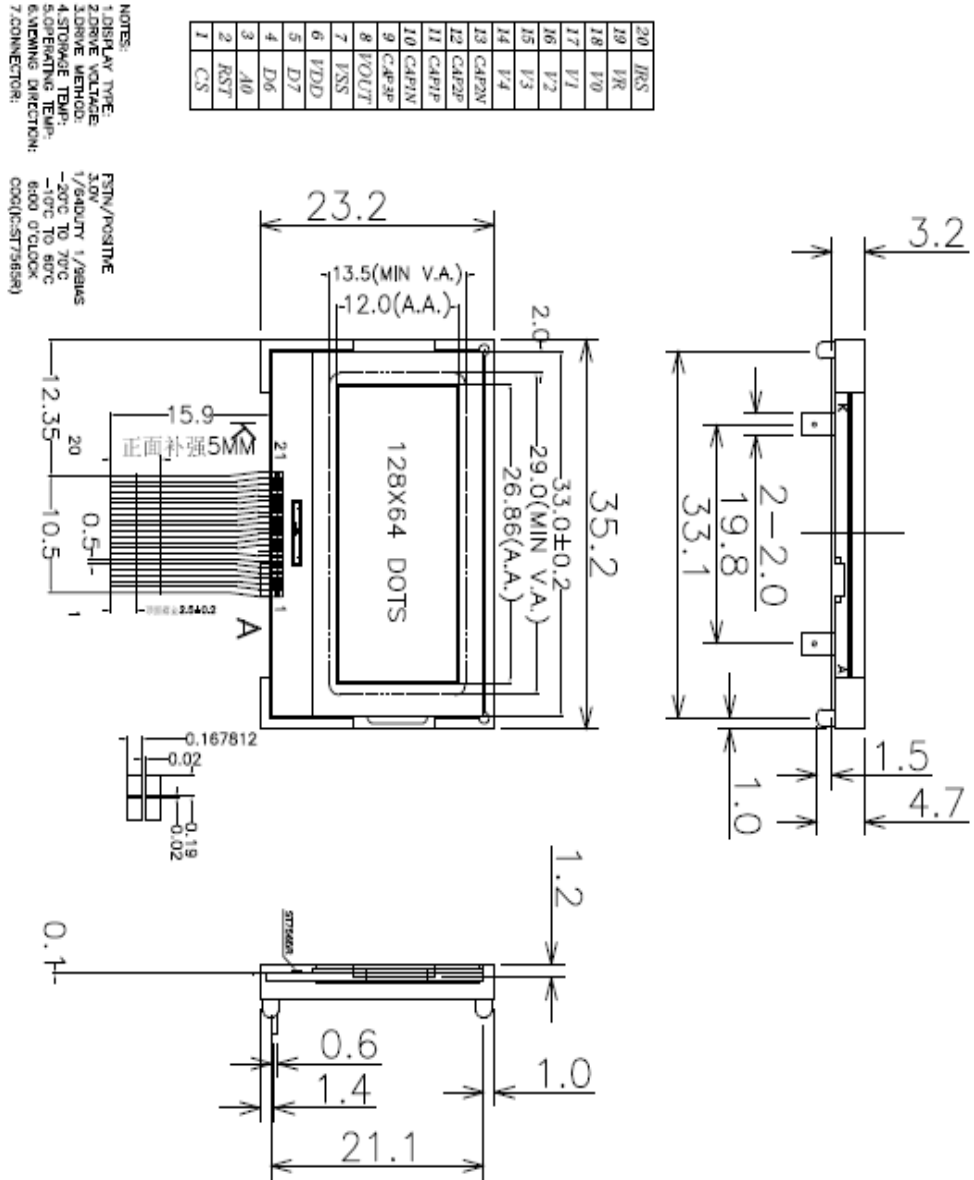


CONTENTS

| | |
|-----------------------------------|-----------|
| Dimensional Outline | 4 |
| Functions & Features | 5 |
| Mechanical Specifications | 5 |
| Pin Description | 6 |
| Maximum Absolute Limit | 7 |
| Electrical characteristics | 8 |
| AC Characteristics | 9 |
| Rererence Applications | 12 |



1. DIMENSIONAL OUTLINE



2. 电路图 CIRCUIT DIAGRAM (LED 1X1=1 dies)





2.FUNCTIONS & FEATURES

| | |
|------------------------|-----------------------|
| 2-1. Format | : 128 *64dots |
| 2-2. LCD mode | : STN, Positive Mode |
| 2-3. Viewing direction | : 6 o'clock |
| 2-4. Driving scheme | : 1/64Duty , 1/9 Bias |
| 2-5. Driver IC | : ST7565R |

3.MECHANICAL SPECIFICATIONS

| | |
|-------------------|----------------------------------|
| 3-1. Module size | 35.20mm(L)*23.20mm(W) *3.20mm(H) |
| 3-2. Viewing area | 29.00mm(L)*13.50mm(W) |



4. PIN DESCRIPTION

| Pin no. | Symbol | Function(parallel) |
|---------|--------|--|
| 20 | IRS | This terminal selects the resistors for the V0 voltage level IRS="H" use the internal resistor IRS="L" not use the internal |
| 19 | VR | Output voltage regulator terminal. Provides the voltage between VSS and V0 through a resistive voltage divider. |
| 18 | V0 | This is a multi-level power supply for the liquid crystal drive. |
| 17 | V1 | |
| 16 | V2 | |
| 15 | V3 | |
| 14 | V4 | |
| 13 | CAP2- | DC/DC voltage converter. Connect a capacitor between this terminal and the CAP2P terminal. |
| 12 | CAP2+ | DC/DC voltage converter. Connect a capacitor between this terminal and the CAP2N terminal. |
| 11 | CAP1+ | DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1N terminal. |
| 10 | CAP1- | DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1P terminal. |
| 9 | CAP3+ | DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1N terminal. |
| 8 | VOUT | DC/DC voltage converter output. |
| 7 | VSS | Groud |
| 6 | VDD | Power supply |
| 5 | SDA | Serial data input |
| 4 | SCL | Serial clock input |
| 3 | A0 | This is connect to the least significant bit of the normal MPU address bus, and it determines whether the data bits are data or command. |
| 2 | RST | When/RES is set to "L", the settings are initialized |
| 1 | CS | This is the chip select signal. |



5. MAXIMUM ABSOLUTE LIMIT (T=25°C)

Unless otherwise noted, $V_{SS} = 0V$

Table 17

| Parameter | Symbol | Conditions | Unit | |
|-------------------------------------|----------------------|---------------|-------------|----|
| Power Supply Voltage | VDD | -0.3 ~ 3.6 | V | |
| Power supply voltage (VDD standard) | VDD2 | -0.3 ~ 3.6 | V | |
| Power supply voltage (VDD standard) | V_0, V_{OUT} | -0.3 ~ 13.5 | V | |
| Power supply voltage (VDD standard) | V_1, V_2, V_3, V_4 | -0.3 to V_0 | V | |
| Operating temperature | T_{OPR} | -30 to +85 | °C | |
| Storage temperature | Bare chip | T_{STR} | -65 to +150 | °C |

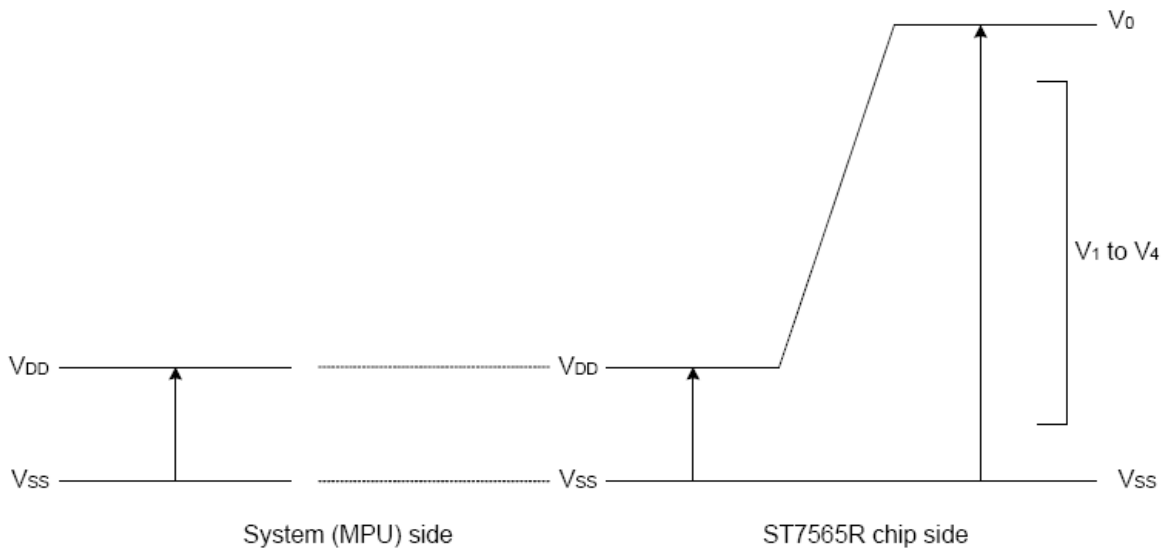


Figure 30

Notes and Cautions

1. The V_{DD2} , V_0 to V_4 and V_{OUT} are relative to the $V_{SS} = 0V$ reference.
2. Insure that the voltage levels of V_1 , V_2 , V_3 , and V_4 are always such that $V_{OUT} \geq V_0 \geq V_1 \geq V_2 \geq V_3 \geq V_4$.
3. Permanent damage to the LSI may result if the LSI is used outside of the absolute maximum ratings. Moreover, it is recommended that in normal operation the chip be used at the electrical characteristic conditions, and use of the LSI outside of these conditions may not only result in malfunctions of the LSI, but may have a negative impact on the LSI reliability as well.



6.ELECTRICAL CHARACTERISTICS

Unless otherwise specified, $V_{SS} = 0\text{ V}$, $V_{DD} = 3.0\text{ V}$, $T_a = -30\text{ to }85^\circ\text{C}$

Table 18

| Item | Symbol | Condition | Rating | | | Units | Applicable Pin | | | |
|-------------------------------------|---------------------|--|---------------------|-----------|---------------------|------------------|-------------------|----|-----|-----|
| | | | Min. | Typ. | Max. | | | | | |
| Operating Voltage (1) | V_{DD} | | 1.8 | — | 3.3 | V | V_{DD}^*1 | | | |
| Operating Voltage (2) | V_{DD2} | (Relative to V_{SS}) | 2.4 | — | 3.3 | V | V_{DD} | | | |
| High-level Input Voltage | V_{IHC} | | $0.8 \times V_{DD}$ | — | V_{DD} | V | *3 | | | |
| Low-level Input Voltage | V_{ILC} | | V_{SS} | — | $0.2 \times V_{DD}$ | V | *3 | | | |
| High-level Output Voltage | V_{OHC} | $I_{OH} = -0.5\text{ mA}$ | $0.8 \times V_{DD}$ | — | V_{DD} | V | *4 | | | |
| Low-level Output Voltage | V_{OLC} | $I_{OL} = 0.5\text{ mA}$ | V_{SS} | — | $0.2 \times V_{DD}$ | V | *4 | | | |
| Input leakage current | I_{LI} | $V_{IN} = V_{DD}\text{ or }V_{SS}$ | -1.0 | — | 1.0 | μA | *5 | | | |
| Output leakage current | I_{LO} | $V_{IN} = V_{DD}\text{ or }V_{SS}$ | -3.0 | — | 3.0 | μA | *6 | | | |
| Liquid Crystal Driver ON Resistance | R_{ON} | $T_a = 25^\circ\text{C}$ (Relative to V_{SS}) $V_0 = 13.0\text{ V}$ $V_0 = 8.0\text{ V}$ | — | 2.0 | 3.5 | $\text{K}\Omega$ | SEn COMn *7 | | | |
| | | | — | 3.2 | 5.4 | | | | | |
| Static Consumption Current | I_{SSQ} | $V_0 = 13.0\text{ V}$ (Relative To V_{SS}) | — | 0.01 | 2 | μA | V_{DD}, V_{DD2} | | | |
| Output Leakage Current | I_{OQ} | | — | 0.01 | 10 | μA | V_0 | | | |
| Input Terminal Capacitance | C_{IN} | $T_a = 25^\circ\text{C}$, $f = 1\text{ MHz}$ | — | 5.0 | 8.0 | pF | | | | |
| Oscillator Frequency | Internal Oscillator | f_{OSC} | 1/65 duty | Ta = 25°C | 1/33 duty | 17 | 20 | 24 | kHZ | *8 |
| | External Input | | | | | f_{CL} | 17 | 20 | 24 | kHZ |
| | Internal Oscillator | f_{OSC} | 1/49 duty | Ta = 25°C | 1/53 duty | 25 | 30 | 35 | kHZ | *8 |
| | External Input | | | | | f_{CL} | 25 | 30 | 35 | kHZ |

Table 19

| Item | Symbol | Condition | Rating | | | Units | Applicable Pin | |
|----------------|---|-----------|---|------|------|-------|----------------|-----------|
| | | | Min. | Typ. | Max. | | | |
| Internal Power | Input voltage | V_{DD2} | (Relative To V_{SS}) | 2.4 | — | 3.3 | V | V_{DD} |
| | Supply Step-up output voltage Circuit | V_{OUT} | (Relative To V_{SS}) | — | — | 13.5 | V | V_{OUT} |
| | Voltage regulator Circuit Operating Voltage | V_{OUT} | (Relative To V_{SS}) | 6.0 | — | 13.5 | V | V_{OUT} |
| | Voltage Follower Circuit Operating Voltage | V_0 | (Relative To V_{SS}) | 4.0 | — | 13.5 | V | V_0^*9 |
| | Base Voltage | VRS | $T_a = 25^\circ\text{C}$, (Relative To V_{SS}) -0.05%/°C | 2.07 | 2.10 | 2.13 | V | *10 |



7.AC CHARACTERISTICS

The 4-line SPI Interface

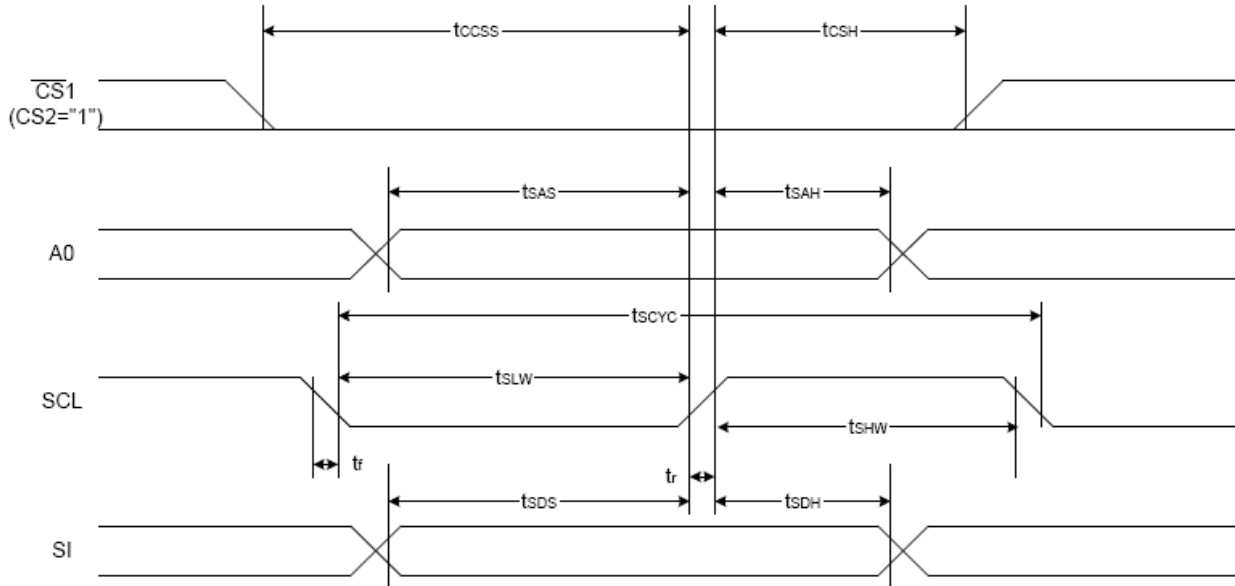


Figure 39

Table 30

($V_{DD} = 3.3V, T_a = -30$ to $85^\circ C$)

| Item | Signal | Symbol | Condition | Rating | | Units |
|-------------------------|--------|------------|-----------|--------|------|-------|
| | | | | Min. | Max. | |
| 4-line SPI Clock Period | SCL | T_{scyc} | | 50 | — | ns |
| SCL "H" pulse width | | T_{shw} | | 25 | — | |
| SCL "L" pulse width | | T_{slw} | | 25 | — | |
| Address setup time | A0 | T_{sas} | | 20 | — | |
| Address hold time | | T_{sah} | | 10 | — | |
| Data setup time | SI | T_{sds} | | 20 | — | |
| Data hold time | | T_{sdh} | | 10 | — | |
| CS-SCL time | CS | T_{css} | | 20 | — | |
| CS-SCL time | | T_{csh} | | 40 | — | |

Table 31

($V_{DD} = 2.7V, T_a = -30$ to $85^\circ C$)

| Item | Signal | Symbol | Condition | Rating | | Units |
|-------------------------|--------|------------|-----------|--------|------|-------|
| | | | | Min. | Max. | |
| 4-line SPI Clock Period | SCL | T_{scyc} | | 100 | — | ns |
| SCL "H" pulse width | | T_{shw} | | 50 | — | |
| SCL "L" pulse width | | T_{slw} | | 50 | — | |
| Address setup time | A0 | T_{sas} | | 30 | — | |
| Address hold time | | T_{sah} | | 20 | — | |
| Data setup time | SI | T_{sds} | | 30 | — | |
| Data hold time | | T_{sdh} | | 20 | — | |
| CS-SCL time | CS | T_{css} | | 30 | — | |
| CS-SCL time | | T_{csh} | | 60 | — | |



8.REFERENCE APPLICATIONS

The ST7565R Series can be connected to either 80X86 Series MPUs or to 68000 Series MPUs. Moreover, using the interface it is possible to operate the ST7565R series chips with fewer signal lines. The display area can be enlarged by using multiple ST7565R Series chips. When this is done, the chip select is used to select the individual ICs to access.

(1) 8080 Series MPUs

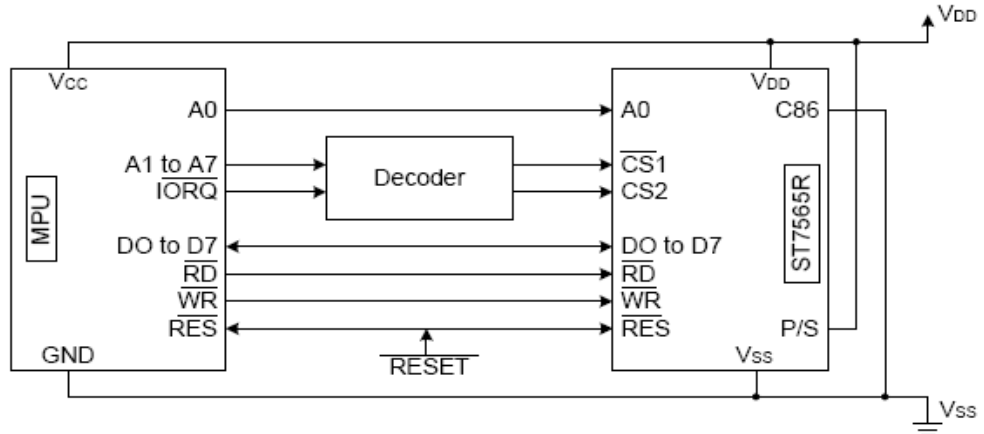


Figure 42-1

(2) 6800 Series MPUs

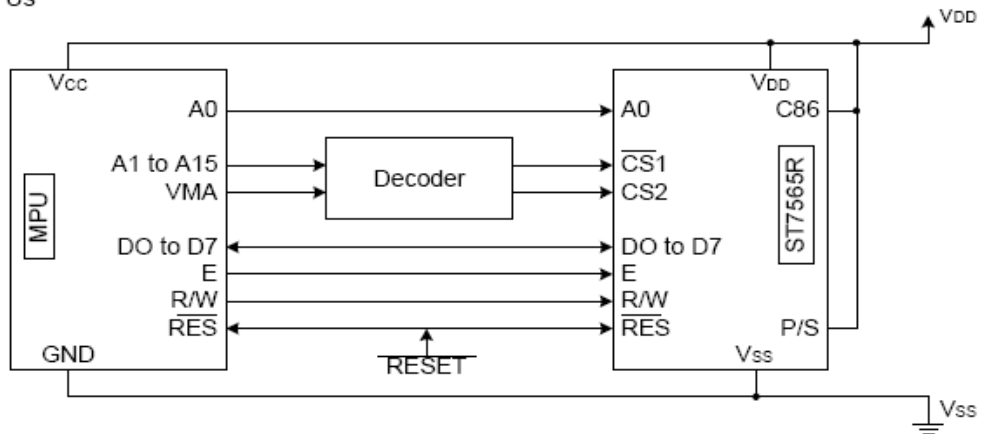


Figure 42-2

(3) Using the 4-line SPI Interface

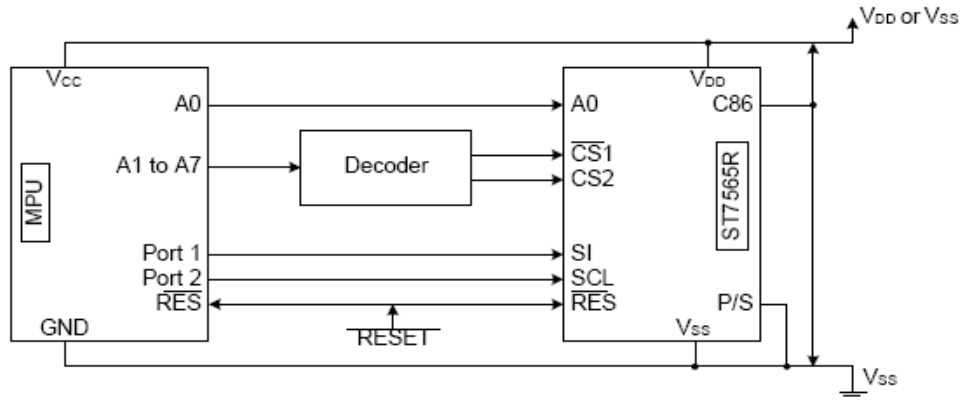
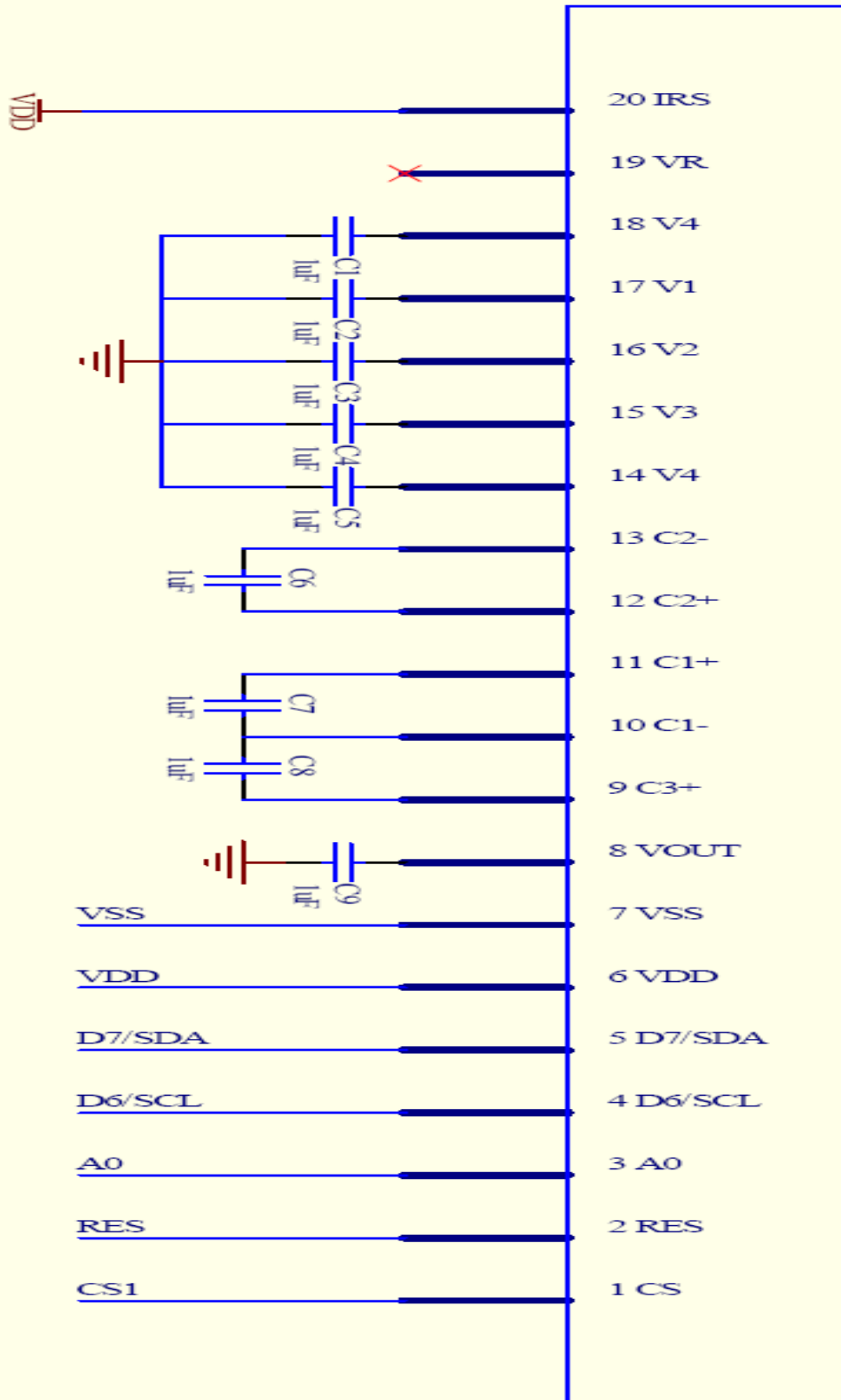


Figure 42-3





深圳市晶惠迪电子有限公司
SHENZHEN JHDLCM ELECTRONIC CO., LTD

```
/*  
/*      IC:ST7565R      */  
/*      Date:10-03-23      */  
/*  
/*  
/*      Head File Define      */  
/*  
#include <reg51.h>  
#include <E:\driver_prg\image\char.h>  
//#include <math.h>  
//#include <intrins.h>  
  
/*  
/*      Interface Define      */  
/*  
sbit      RESET=P3^0;  
sbit      CD=P3^4;  
sbit      CS=P3^2;  
sbit      SCL=P1^6;  
sbit      SDA=P1^7;  
sbit      KEY_PRESS=P3^3;  
sbit      DEC_KEY=P3^1;  
sbit      EXIT_KEY=P3^7;  
/*  
/*      Parameter Define      */  
/*  
#define   CONTRAST=0x0C  
#define   DATA0=0x40  
#define   DATA1=0x41  
unsigned char   page_width;  
unsigned char   seg_width;  
  
unsigned char   Buf_1,Buf_2,Buf_3,conversion;  
unsigned char   *char_point;  
int   contrast;  
/*  
/*      Picture data      */  
/*  
unsigned char   code   row_table[]={0x01,0x02,0x04,0x08,0x10,0x20,0x40,0x80};  
  
unsigned char   code   char_0[]={ 62,65,65,62,0 };  
unsigned char   code   char_1[]={ 0,66,127,64,0 };
```



深圳市晶惠迪电子有限公司
SHENZHEN JHDLCM ELECTRONIC CO., LTD

```
unsigned char code char_2[]={98,81,73,70,0};  
unsigned char code char_3[]={34,73,73,54,0 };  
unsigned char code char_4[]={28,18,127,16,0 };  
unsigned char code char_5[]={39,69,69,57,0};  
unsigned char code char_6[]={62,73,73,50,0};  
unsigned char code char_7[]={1,121,7,1,0};  
unsigned char code char_8[]={54,73,73,54,0 };  
unsigned char code char_9[]={6,73,73,62,0};  
unsigned char code orise_tech[]={
```

```
/*  
/*      Time Delay      */  
*/
```

```
void Delay(long i)  
{  
    while(i!=0)  
    {  
        i--;  
    }  
}
```

```
/*  
/*      Transfer Commmand      */  
*/
```

```
void Write_Command(unsigned char command)  
{  
    int i,j;  
    j=0x80;  
    CS=0;  
    CD=0;  
    for(i=0;i<8;i++)  
    {  
        SCL=0;  
        if(command&j)SDA=1;  
        else SDA=0;  
        SCL=1;  
        j=j>>1;  
    }  
    CS=1;  
}
```



深圳市晶惠迪电子有限公司
SHENZHEN JHDLCM ELECTRONIC CO., LTD

```

/*****/
/*      Transfer Data      */
/*****/
void Write_Data(unsigned char data1)
{
    int i,j;
    j=0x80;
    CS=0;
    CD=1;
    for(i=0;i<8;i++)
    {
        SCL=0;
        if(data1&j)SDA=1;
        else SDA=0;
        SCL=1;
        j=j>>1;
    }
    CS=1;
}
//-----
void Set_column_addr(unsigned char add)
{
    unsigned char temp;
    temp=add;
    add=add>>4;
    add=add&0x0f;
    add=add|0x10;
    Write_Command(add); //Set upper addr;
    add=temp;
    add=add&0x0F;
    Write_Command(add); //Set lower addr;
}
//-----
void Set_row_addr(unsigned char row)
{
    row=row&0x0F;
    row=row|0x0B0;
    Write_Command(row);    //page addr set
}
/*****/
/*      LCD Initial Code      */
/*****/
void Lcd_Set()
```



深圳市晶惠迪电子有限公司
SHENZHEN JHDLCM ELECTRONIC CO., LTD

```

{
    RESET=0;
    Delay(1000);
    RESET=1;
    Delay(1000);
    Write_Command(0xA2);           //Set Bias           0xA2
    Write_Command(0xA0);           //Segment Direction Select ,bit0=1,reverse;=0,normal;
    Write_Command(0xC8);           //Common Direction Select,bit3=1,reverse
direction;=0,normal;
    Write_Command(0xAC);
    Write_Command(0xA7);           //bit0=0,Normal/bit0=1,Reverse Display
    Write_Command(0xA4);
    Write_Command(0x2C);           //Power Control Set
    Delay(10);
    Write_Command(0x2E);           //Power Control Set
    Delay(10);
    Write_Command(0x2F);           //Power Control Set
    Delay(10);
    Write_Command(0x24);           //set ra/rb 0x25
    Write_Command(0x81);           //Set Contrast
    Write_Command(0x27);           //29
    Write_Command(0xD5);
    Write_Command(0x00);
    Write_Command(0xD2);
    Write_Command(0x00);
    Write_Command(0x60);           //Set Display Start Line    40
    Write_Command(0xb0);
    Write_Command(0x10);
    Write_Command(0x00);
    Write_Command(0xAF);           //Display ON
}

```

```

/*****/
/*      Clear Display      */
/*****/

```

```
void Display_Clear(data1,data2)
```

```

{
    int i,j,m;
    m=0xb0;
    for(i=0;i<9;i++)
    {
        Write_Command(m);

```



```
Write_Command(0x10);
Write_Command(0x00);
for(j=0;j<96;j++)
{
    Write_Data(data1);
    Write_Data(data2);
}
m++;
}
}
/*****/
void show_three_h_bar(int row)
{
    int i,page,row_data;
    page=row/8;
    i=row%8;
    row_data=row_table[i];

    Set_row_addr(page-1);    //清除前一条横线
    Set_column_addr(00);
    for(i=0;i<192;i++)
    {
        Write_Data(0x00);
    }
    Set_row_addr(page);    //清除前一条横线
    Set_column_addr(00);
    for(i=0;i<192;i++)
    {
        Write_Data(0x00);
    }

    Set_row_addr(page);
    Set_column_addr(00);
    for(i=0;i<192;i++)
    {
        Write_Data(row_data);
    }
}
/*****/
/*****/
/*      three vertical bar      */
/*****/
void show_three_v_bar(int bar)
```




```
{
    int i;
    Set_column_addr(bar-1);
    for(i=0;i<9;i++)
        {
            Set_row_addr(i);
            Set_column_addr(bar-1);
            Write_Data(0x00);
        }
    Set_column_addr(bar);
    for(i=0;i<9;i++)
        {
            Set_row_addr(i);
            Set_column_addr(bar);
            Write_Data(0x0ff);
        }
    for(i=0;i<9;i++)
        {
            Set_row_addr(i);
            Set_column_addr(bar+1);
            Write_Data(0x0ff);
        }
    for(i=0;i<9;i++)
        {
            Set_row_addr(i);
            Set_column_addr(bar+2);
            Write_Data(0x0ff);
        }
}
/*****/
/*****/
/*      one vertical bar      */
/*****/
void show_one_v_bar(int bar)
{
    int i;
    Set_column_addr(bar-1);
    for(i=0;i<9;i++)
        {
            Set_row_addr(i);
            Set_column_addr(bar-1);
            Write_Data(0x00);
        }
}
```



```
Set_column_addr(bar);
for(i=0;i<9;i++)
{
    Set_row_addr(i);
    Set_column_addr(bar);
    Write_Data(0x0ff);
}
}
/*****/
/*      one horizontal bar      */
/*****/
void show_one_h_bar(int row)
{
    int i,page,row_data;
    page=row/8;
    i=row%8;
    row_data=row_table[i];

    Set_row_addr(page-1);    //清除前一条横线
    Set_column_addr(00);
    for(i=0;i<132;i++)
    {
        Write_Data(0x00);
    }
    Set_row_addr(page);    //清除前一条横线
    Set_column_addr(00);
    for(i=0;i<192;i++)
    {
        Write_Data(0x00);
    }

    Set_row_addr(page);
    Set_column_addr(00);
    for(i=0;i<192;i++)
    {
        Write_Data(row_data);
    }
}
/*****/
/*      Display Font      */
/*****/
void Display_Font(unsigned char page0,seg0,unsigned char *p)
{
```



```
int i,m,n;
m=((seg0&0xf0)>>4)|0x10;
n=seg0&0x0f;
Write_Command(page0);
Write_Command(m);
Write_Command(n);
for(i=0;i<5;i++)
{
    Write_Data(*p++);
}
}

/*****/
/*      Display Hanzi      */
/*****/
void Display_Hanzi(unsigned char page0,seg0,unsigned char *p)
{
    int i,m,n;
    m=((seg0&0xf0)>>4)|0x10;
    n=seg0&0x0f;
    Write_Command(page0);
    Write_Command(m);
    Write_Command(n);
    for(i=0;i<16;i++)
    {
        Write_Data(*p++);
    }
    m=((seg0&0xf0)>>4)|0x10;
    n=seg0&0x0f;
    Write_Command(page0+1);
    Write_Command(m);
    Write_Command(n);
    for(i=0;i<16;i++)
    {
        Write_Data(*p++);
    }
}

/*****/
/*      Display Character      */
/*****/
void Display_Character(unsigned char page0,seg0,unsigned char *p0)
{
```



```
int step0=10;
int step1=5;
Display_Font(page0,seg0,p0);
seg0=seg0+step0;
p0=p0+step1;
Display_Font(page0,seg0,p0);
seg0=seg0+step0;
p0=p0+step1;
Display_Font(page0,seg0,p0);
seg0=seg0+step0;
p0=p0+step1;
Display_Font(page0,seg0,p0);
seg0=seg0+step0;
p0=p0+step1;
Display_Font(page0,seg0,p0);
seg0=seg0+step0;
p0=p0+step1;
Display_Font(page0,seg0,p0);
seg0=seg0+step0;
p0=p0+step1;
Display_Font(page0,seg0,p0);
seg0=seg0+step0;
p0=p0+step1;
Display_Font(page0,seg0,p0);
seg0=seg0+step0;
p0=p0+step1;
Display_Font(page0,seg0,p0);
seg0=seg0+step0;
p0=p0+step1;
Display_Font(page0,seg0,p0);
seg0=seg0+step0;
p0=p0+step1;
Display_Font(page0,seg0,p0);
seg0=seg0+step0;
p0=p0+step1;
Display_Font(page0,seg0,p0);
/*
seg0=seg0+step0;
p0=p0+step1;
Display_Font(page0,seg0,p0);
```




深圳市晶惠迪电子有限公司
SHENZHEN JHDLCM ELECTRONIC CO., LTD

```
int i;
Set_column_addr(xs);    //column addr set
Set_row_addr(page);
for(i=0;i<5;i++)      //5X8 font
{
    Write_Data(*(p++));
    //Write_Data(*(p++));
}
}
//-----

//-----
void Conversion_T0_char(int number)
{
    Buf_1=number/100;
    conversion=number%100;
    Buf_2=conversion/10;
    Buf_3=conversion%10;
}
//-----

char *Get_addr(int num)
{
    unsigned char *pt;
    switch(num)
    {
        case 0: pt=char_0;break;
        case 1: pt=char_1;break;
        case 2: pt=char_2;break;
        case 3: pt=char_3;break;
        case 4: pt=char_4;break;
        case 5: pt=char_5;break;
        case 6: pt=char_6;break;
        case 7: pt=char_7;break;
        case 8: pt=char_8;break;
        case 9: pt=char_9;break;
    }
    return pt;
}
//-----

void show_three_number(unsigned char x,y,int n)
{
```

```
    Conversion_T0_char(n);
```

```
    char_point=Get_addr(Buf_1);
```

22

SHENZHEN JHDLCM ELECTRONIC CO.,LTD

**Address: Bao'an District of Shenzhen City West township Hok Chau Hang Fung
Industrial City, 14th floor, Building B, C6, China**

Tel: +86-755-29067605/29067625 FAX: +86-755-27364864 mail: jhdlcm@163.com



```
Show_one_char(x,y,char_point);
char_point=Get_addr(Buf_2);
x=x+5;
Show_one_char(x,y,char_point);
char_point=Get_addr(Buf_3);
x=x+5;
Show_one_char(x,y,char_point);
}
/*****/
/*****/
void Wait_Press()
{
    while(KEY_PRESS);
    Delay(0x5000);
}
/*****/
/*      Draw a image      */
/*****/
void Display_Image(unsigned char page0,seg0,pagew,segw,unsigned char *p)
{
    int i,j,m,n;
    m=((seg0&0xf0)>>4)|0x10;
    n=seg0&0x0f;
    for(i=0;i<pagew;i++)
    {
        Write_Command(page0);
        Write_Command(m);
        Write_Command(n);
        for(j=0;j<segw;j++)
        {
            Write_Data(*(p++));
        }
        page0++;
    }
}
/*****/
//REGULATE CONTRAST
void Regulate_contrast()
{
    while(EXIT_KEY&&01)
    {
        if(KEY_PRESS==0)
```



深圳市晶惠迪电子有限公司
SHENZHEN JHDLCM ELECTRONIC CO., LTD

```
{
    contrast=contrast+1;
    Write_Command(0x81);           //Set Contrast
    Write_Command(contrast);
    Delay(0x5000);
}
if(DEC_KEY==0)
{
    contrast=contrast-1;
    Write_Command(0x81);           //Set Contrast
    Write_Command(contrast);
    Delay(0x5000);
}
}
}
/*****/
/*      Main      */
/*****/
void main()
{
    int i;
    int j=0x40;
    contrast=0x0C;
    Lcd_Set();
    while(1)
    {

        Display_Image(0xb0,0x01,8,128,description1);
        Wait_Press(); //Delay(0x5000);
        Display_Image(0xb0,0x01,8,128,description2);
        Wait_Press(); //Delay(0x5000);
        Display_Image(0xb0,0x01,8,128,description3);
        Wait_Press(); //Delay(0x5000);
        Display_Image(0xb0,0x01,8,128,description4);
        Wait_Press(); //Delay(0x5000);

        Display_Image(0xb0,0x01,8,128,description5);
        Wait_Press(); //Delay(0x5000);

        Display_Clear(0x00,0x00);
        Display_Clear(0x55,0xaa);           //SNOW
```




深圳市晶惠迪电子有限公司
SHENZHEN JHDLCM ELECTRONIC CO., LTD

```
Wait_Press();
Display_Clear(0x00,0x00);
Display_Clear(0xFF,0xFF);           //BLACK
Wait_Press();

Display_Clear(0x00,0x00);
Display_Clear(0xff,0x00);           //V_BAR
Wait_Press();

Display_Clear(0x00,0x00);
Display_Clear(0x55,0x55);           //H_BAR
Wait_Press();

Display_Clear(0x00,0x00);
//Display_Clear(0xFF,0xFF);         //BLACK
for(i=0;i<65;i++)
{
    show_one_h_bar(i);
    show_three_number(4,3,i);
    Wait_Press();
}
}
→
```